

IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently amended) A current driver circuit for driving a pair of transmission lines by allowing a current to flow in a terminal resistor connected between the pair of transmission lines, the current driver circuit comprising:

a current driver having a current source transistor connected to a power source potential level, the current driver being coupled to the pair of transmission lines; and

a current compensation circuit for compensation of an output current from the current driver in response to a common mode potential of the pair of transmission lines, the current compensation circuit being coupled to an output side of the current source transistor, and

wherein the current compensation circuit increases the output current by an amount substantially equal to the amount of reduction in the output current caused by the current source transistor entering a non-saturated region.

2-3. (Canceled)

4. (Original) The current driver circuit of claim 1, wherein the current compensation circuit comprises:

a plurality of resistors connected in series between the power source potential level and a ground level;

a comparator for making a comparison between a terminal potential of each of the plurality of resistors and the common mode potential;

a load transistor activated by a reversal of a magnitude relationship between the common mode potential inputted to the comparator and the terminal potential; and

a Miller transistor for supplying, to the current driver, a current directly proportional to a current flowing in the load transistor.

5. (Original) The current driver circuit of claim 4, wherein the current compensation circuit further comprises a transistor provided between the power source potential level and the ground level and connected in series to the plurality of resistors.

6. (Original) The current driver circuit of claim 4, wherein
the comparator comprises a first nMOS transistor having a gate to which the common mode potential is inputted, a second nMOS transistor having a gate to which the terminal potential is inputted, and a first current source having one end connected to each of the first and second nMOS transistors,

the load transistor is composed of a pMOS transistor connected in parallel to a second current source and having one end connected to the first nMOS transistor, and

the current supplied from the Miller transistor to the current driver is given by:

$$\{\beta \times (V_{gs} - V_t)^2 - I\} \times \alpha$$

(where V_{gs} is a potential difference between a source and the gate of the first nMOS transistor, V_t is a threshold voltage of the first nMOS transistor, β is a constant determined by a channel width and a channel length of the first nMOS transistor, I is a current flowing in the second current source, and α is a Miller ratio of a drain current of the Miller transistor to a drain current of the load transistor).

7. (Canceled)

8. (Currently amended) A current driver circuit for driving a pair of transmission lines by allowing a current to flow in a terminal resistor connected between the pair of transmission lines, the current driver circuit comprising:

a current driver having a plurality of switch circuits for controlling a current flowing in the pair of transmission lines; and

a current compensation circuit for controlling stepwise respective operations of the plurality of switch circuits in response to a variation in a common mode potential of the pair of transmission lines, and

wherein the current compensation circuit operates activate the plurality of switch circuits on an individual basis as a difference between a power source potential level and the common mode potential is reduced.

9-10. (Canceled)

11. (Original) The current driver circuit of claim 8, wherein the specified one of the plurality of switch circuits comprises a resistor connected in series between a switch element composing the specified switch circuit and one of the pair of transmission lines.

12. (Original) The current driver circuit of claim 8, wherein each of switch elements composing the plurality of switch circuits is composed of a MOS transistor and the MOS transistor has a gate connected to a ground level via a given resistor.

13. (Canceled)

14. (New) The current driver circuit of claim 8, wherein plurality of switch circuits are coupled together in a parallel configuration.